

PRODUCT DATASHEET

Stellaria

Edge-AI-optimized FPGA module combining a Lattice FPGA with integrated BrainChip Akida® neuromorphic IP accelerator.

Hardware profile

Family	Roadmap tiles
Category	In-development AI tile/module
Status	In development
Form factor	Tile/module (size not stated)
Radiation positioning	Fault-tolerant, ultra-low-power AI inferencing positioning
Primary target	Real-time decision-making, anomaly detection, autonomous operations, event-driven processing at the spacecraft edge
Source pages	7

Key hardware

Lattice FPGA + BrainChip Akida® neuromorphic IP accelerator

Tile characteristics

Attribute	Value	Source
Characteristic	Lattice FPGA	7
Characteristic	BrainChip Akida® neuromorphic IP accelerator	7
Characteristic	Ultra-low-power AI inferencing	7
Characteristic	Fault-tolerant event-driven processing	7

Applications

Target workload

Use case	Source
	7
	7
	7

Generated from the structured Setanta product catalog. Detailed electrical and mechanical specifications are available through direct mission review where applicable.